



# **DCP51/-16**

#### PNP SURFACE MOUNT TRANSISTOR

### **Features**

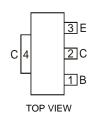
- **Epitaxial Planar Die Construction**
- Complementary NPN Type Available (DCP54)
- Ideally Suited for Automated Assembly Processes
- Ideal for Medium Power Switching or Amplification **Applications**
- Lead Free By Design/RoHS Compliant (Note 1)
  - "Green" Device (Note 2)

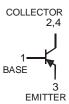
#### **Mechanical Data**

- Case: SOT-223
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminals: Finish Matte Tin annealed over Copper leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Marking & Type Code Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.115 grams (approximate)



SOT-223





Schematic and Pin Configuration

## **Maximum Ratings** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	-45	V
Collector-Emitter Voltage	V <sub>CEO</sub>	-45	V
Emitter-Base Voltage	V <sub>EBO</sub>	-5	V
Peak Pulse Current	I <sub>CM</sub>	-1.5	A
Continuous Collector Current	Ic	-1	A

### **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation @ T <sub>A</sub> = 25°C (Note 3)	$P_d$	1 (Note 3)	W
Operating and Storage Temperature Range	$T_{j,}T_{STG}$	-55 to +150	°C
Thermal Resistance Junction to Ambient Air @ T <sub>A</sub> = 25°C (Note 3)	$R_{\theta JA}$	125	°C/W

## Electrical Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Conditions
OFF CHARACTERISTICS (Note 4)						
Collector-Base Breakdown Voltage	V <sub>(BR)CBO</sub>	-45	_	_	V	$I_C = -100 \mu A, I_E = 0A$
Collector-Emitter Breakdown Voltage	V <sub>(BR)CEO</sub>	-45	_	_	V	$I_C = -10 \text{mA}, I_B = 0 \text{A}$
Emitter-Base Breakdown Voltage	V <sub>(BR)EBO</sub>	-5	_	_	V	$I_E = -10\mu A, I_C = 0A$
Collector Cut-Off Current	1	_	_	-100	nA	$V_{CB} = -30V, I_{E} = 0A$
	I <sub>CBO</sub>		_	-10	μΑ	$V_{CB} = -30V$ , $I_E = 0A$ , $T_A = 150$ °C
Emitter Cut-Off Current	I <sub>EBO</sub>	_	_	-10	μА	$V_{EB} = -5V, I_{C} = 0A$
ON CHARACTERISTICS (Note 4)						
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	_	_	-0.5	V	$I_C = -500 \text{mA}, I_B = -50 \text{mA}$
Base-Emitter Turn-On Voltage	$V_{BE(ON)}$	_	_	-1.0	V	$I_C = -500 \text{mA}, V_{CE} = -2 \text{V}$
	<b>L</b>	40		250		$I_C = -150 \text{mA}, V_{CE} = -2 \text{V}$
DC Current Gain	h <sub>FE</sub>	25	_	_	_	$I_C = -500 \text{mA}, V_{CE} = -2 \text{V}$
DCP51-16		100	_	250		$I_C = -150 \text{mA}, V_{CE} = -2 \text{V}$
SMALL SIGNAL CHARACTERISTICS						
Transition Frequency	f <sub>T</sub>	_	200	_	MHz	$I_C = -50 \text{mA}, V_{CE} = -5 \text{V},$ f = 100 MHz

Note:

- 1. No purposefully added lead.
- 2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead\_free/index.php.
  3. Device mounted on FR-4 PCB pad layout as shown on page 4 or on Diodes, Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

www.diodes.com

4. Measured under pulsed conditions. Pulse width =  $300\mu s$ . Duty cycle  $\leq 2\%$ 

DCP51/-16



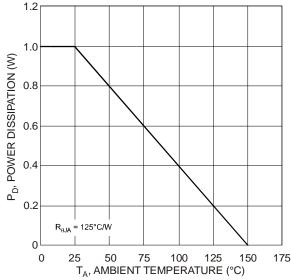


Fig. 1 Power Dissipation vs. Ambient Temperature (Note 3)

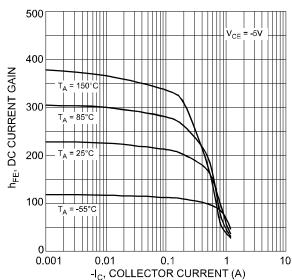


Fig. 3 Typical DC Current Gain vs. Collector Current

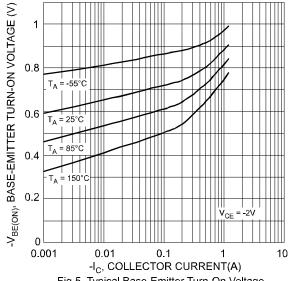


Fig 5. Typical Base-Emitter Turn-On Voltage vs. Collector Current

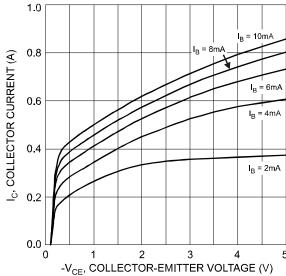


Fig. 2 Typical Collector Current vs. Collector-Emitter Voltage

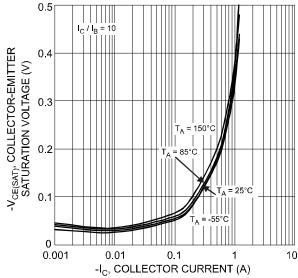


Fig. 4 Typical Collector-Emitter Saturation Voltage vs. Collector Current

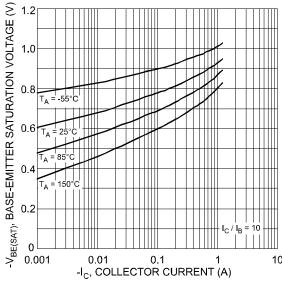
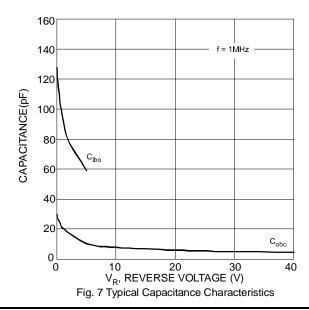


Fig. 6 Typical Base-Emitter Saturation Voltage vs. Collector Current





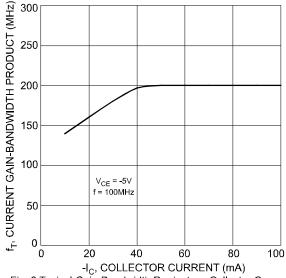


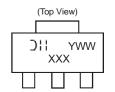
Fig. 8 Typical Gain-Bandwidth Product vs. Collector Current

## Ordering Information (Note 5)

Part Number	Case	Packaging
DCP51-13	SOT-223	2500 / Tape & Reel
DCP51-16-13	SOT-223	2500 / Tape & Reel

Notes: 5. For packaging details, please visit our website at http://www.diodes.com/datasheets/ap02007.pdf.

## **Marking Information**

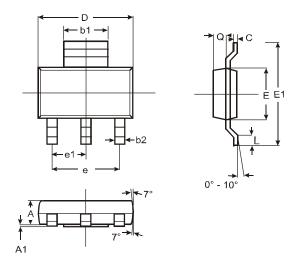


III = Manufacturer's code marking

XXX = Product type marking code Ex:

YWW = Date code marking Y = Last digit of year ex: 7 = 2007 WW = Week code 01 - 52

## **Package Outline Dimensions**



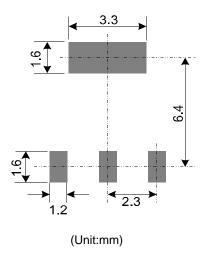
SOT-223					
Dim	Min	Max	Тур		
Α	1.55	1.65	1.60		
A1	0.010	0.15	0.05		
b1	2.90	3.10	3.00		
b2	0.60	0.80	0.70		
С	0.20	0.30	0.25		
D	6.45	6.55	6.50		
Е	3.45	3.55	3.50		
E1	6.90	7.10	7.00		
е	_		4.60		
e1	_	_	2.30		
L	0.85	1.05	0.95		
Q	0.84	0.94	0.89		
All Dimensions in mm					

P14 = DCP51

P14-16 = DCP51-16



## Suggested Pad Layout: (Based on IPC-SM-782)



#### IMPORTANT NOTICE

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to any product herein. Diodes Incorporated does not assume any liability arising out of the application or use of any product described herein; neither does it convey any license under its patent rights, nor the rights of others. The user of products in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on our website, harmless against all damages.

#### LIFE SUPPORT

Diodes Incorporated products are not authorized for use as critical components in life support devices or systems without the expressed written approval of the President of Diodes Incorporated.